

Attorney's Docket No.: 10559-108001
Client's Ref. No.: P7643

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EXAMINER LEE

GROUP 2181 FAX NO: (703) 746-9248 (703) 746-7238

Number of pages including this page 15



Applicant : Keith Dow
Serial No. : 09/461,643
Filed : December 14, 1999

Art Unit : 2181
Examiner : Christopher E. Lee

Title : IMPROVED SIGNAL ROUTING BETWEEN A MEMORY CONTROL UNIT
AND A MEMORY DEVICE

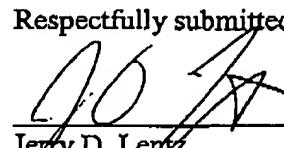
FACSIMILE COMMUNICATION

Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached to this facsimile communication cover sheet is Response, faxed this 26th
day of November, 2002, to Group 2181, of the United States Patent and Trademark Office.

Respectfully submitted,



Jerry D. Lenz
Reg. No. 33,945

Date: November 26, 2002

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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UNIT AND A MEMORY DEVICE

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TRANSMITTAL LETTER

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total

Claims	22	-	22	=	0	\$0
Independent	4	-	4	=	0	\$0
TOTAL FEE DUE						\$0

TOTAL FEE DUE \$0

Respectfully submitted,

Date: 4/26/82

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Entered on appeal
12/4/02 ECR

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BOX AF
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RESPONSE

In response to the action mailed September 5, 2002, please amend the application as follows:

In the specification:

Paragraph beginning at page 1, line 19 has been amended as follows:

-- The speed at which the computer operates depends in large part on the speed at which data is transferred between the processor 105 and the memory unit 110. One memory architecture in particular, known as the RAMBUS™ architecture, is designed to transfer data to the processor 105 at very high rates, e.g., 1.6 GB/s for a typical RAMBUS™ DRAM (RDRAM) module. --

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